



Docket No.: 42390P10751

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

BALAJI PARTHASARATHY, ET AL.

Application No.: 09/835,132

Filed: March 30, 2001

For: Host-Fabric Adapter Having An Efficient Multi-Tasking Pipelined
Instruction Execution Micro-Controller Subsystem For
NGIO/Infiniband™ Applications

Art Group: 2183

Examiner: Ellis, Richard L.

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

11/09/2004 NROCHA1 00000041 09835132

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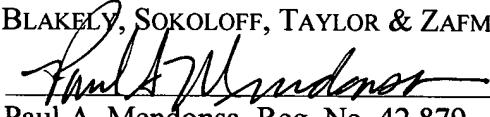
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: November 04, 2004


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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

Sheet 1 of 3

425501 10751

Application Number	09/835,132
Filing Date	March 30, 2001
First Named Inventor	Balaji Parthasarathy
Art Unit	2183
Examiner Name	Ellis, Richard L.
Attorney Docket Number	42390P10751

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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of

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Complete if Known

Application Number	09/835,132
Filing Date	March 30, 2001
First Named Inventor	Balaji Parthasarathy
Art Unit	2183
Examiner Name	Ellis, Richard L.
Attorney Docket Number	42390P10751

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
8	Pending U.S. Patent Application 09/671,707, entitled "Host-Fabric Adapter and Method of Connecting a Host System to a Channel-Based Switched Fabric in a Data Network", filed		
9	Pending U.S. Patent Application 09/603,957, entitled "Device to Receive, Buffer, and Transmit Packets of Data in a Packet Switching Network", filed June 26, 2000, P8329.		
10	Pending U.S. Patent Application 10/842,752, entitled "Device to Receive, Buffer, and Transmit Packets of Data in a Packet Switching Network", filed May 10, 2004, P8329C.		
11	Pending U.S. Patent Application 09/608,645, entitled "Memory Utilization in a Network Interface", filed June 30, 2002, P8330.		
12	Pending U.S. Patent Application 09/669,670, entitled "Host-Fabric Adapter and Method of Connecting a Host System to a Channel-Based Switched Fabric in a Data Network", filed		
13	Pending U.S. Patent Application 09/672,711, entitled "Host-Fabric Adapter Having Hardware Assist Architecture and Method of Connecting a Host System to a Channel-Based		
14	Pending U.S. Patent Application 09/698,188, entitled "Host-Fabric Adapter Having Hardware Assist Architecture and Method of Connecting a Host System to a Channel-Based		

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